

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Takashi Hoshino, et al. Art Unit: 2814
Serial No.: 10/522,278 Examiner: Mursalin B. Hafiz
Filed : January 24, 2005
Title : VERTICAL JUNCTION FIELD EFFECT TRANSISTORS, AND METHODS
OF PRODUCING THE VERTICAL JUNCTION FIELD EFFECT
TRANSISTORS (TO BE AMENDED)

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

NOTICE OF ALLOWANCE DATE: 3/15/2007
CONFIRMATION NO. : 8811

AMENDMENT AFTER ALLOWANCE PURSUANT TO 37 C.F.R. §1.312

Please amend the application as indicated below.

Amendments to the **Title** begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.